

**ABSTRACT**

A stereo demodulator circuit comprising at least one noise control unit for performing a noise control responding to an RSSI (reception electric-field intensity) when the RSSI is within a specified range, further comprises an AD converter unit for AD-converting a signal corresponding to the RSSI and a control signal producing unit for producing a control signal for a noise control performed in the noise control unit according to a noise level when the noise level obtained by the AD conversion is within the above described specified range. The control signal producing unit comprises an offset unit for digitally offsetting a signal obtained through the above described AD conversion by a predefined value and truncating lower bits off the offset value by the number of bits in compliance with a grade of the noise control accuracy and outputs the control signal based on the signal obtained from the offset unit.